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Synthesis of Graphene Nanostructures and Nanocomposites for Memristive Devices

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Dedicated to

My Parents, Family and Friends

&

Pakistan



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Abbreviations and Notations

| Density of states | DOS |
|--|--------|
| Electrohydrodynamic atomization | EHDA |
| Electrohydrodynamic printing | EHD |
| Zero dimensional | 0D |
| One dimensional | 1D |
| Two Dimensional | 2D |
| Graphene oxide | GO |
| Chemical vapor deposition | CVD |
| Reduced graphene oxide | rGO |
| Field-effect transistors | FETs |
| Carbon nanotube | CNT |
| X-ray diffration | XRD |
| Scanning electron microscopy | SEM |
| Photoluminescence | PL |
| N-methyl-pyrrolidone | NMP |
| 1, 2-dichlorobenzene | DCB |
| Propylene glycol methyl ether acetate | PGMEA |
| Poly (4-vinyl phenol) | PVP |
| Selected area electron diffraction | SAED |
| Part per billion | ppb |
| Ultraviolet | UV |
| Resistive switching | RS |
| Compliance current | CC |
| High resistance state | HRS |
| Conducting filament | CF |
| Low resistance state | LRS |
| Transition metal dichalcogenides | TMDs |
| Metal atoms (M) and chalcogen atoms (X) | MX_2 |
| Infrared | IR |
| High resolution transmission electron microscopy | HRTEM |
| | |



| Langmuir–Blodgett | LB |
|--------------------------|------|
| Silicon nanowire | SiNW |
| Nanostructured materials | NSMs |



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ABSTRACT

Graphene has had its share of fame and promise for fair part of our technological wonder and has kept churning out technological breakthroughs one after the other in past decade. The industry and academia are focused to discover and harness all the possible revelations this wonderful allotrope of carbon has on offer. Graphene and other members of 2D material family have been researched for wide range of properties such as photonic, electronic and mechanical properties. Resistive switching in materials has been studied in one way or the other since 1960s. However memristive devices have gained a lot of interest after experimental fabrication of the memristive device by Hewlett-Packard labs proving the mathematical proposition by Chua. This work is focused on synthesis of graphene nanostructures in form of nanoflakes and quantum dots, and the composites with polymers. The electrohydrodynamic atomization and other printed technologies were employed to fabricate resistive switching devices on PET and glass as substrate materials. The futuristic flexible printed memristors have also been explored and reported in this work. The technique employed to synthesize graphene quantum dots was used for synthesis of luminescent quantum dots of MoS₂ as well. Thus providing a promising technique worth further exploration for synthesis of quantum dots of other 2D materials.



Chapter 1. INTRODUCTION

Ever since the discovery of low dimensional carbon nanomaterials, fullerene (0D) and single walled carbon nanotube (CNT) (1D) several researchers attempted to isolate 2D graphitic material. The efforts paid off in 2004, in form of breakthrough in isolation and fabrication of graphene based transistor¹. Graphene has since become a shining star on the horizon of materials science and condensed-matter physics. The fame and zeal for exploration undertaken for graphene has been unmatched for any material in history. It has exceptionally high crystal and electronic quality, and has already empowered new physics and potential applications, despite a very short time since its discovery by Geim and Novoselov² in 2004 while studying thin carbon films. Contradicting to the 70 years old argument by Landau and Peierls^{3,4} that strictly two dimensional (2D) crystals were thermodynamically unstable and could not exist, graphene is here with proven proof for significance in terms of fundamental physics.



Figure 1-1: A C_{60} fullerene molecule, a carbon nanotube and graphite are in the order from left to right. Graphene, a single sheet of graphite, can be considered as building block of all these carbon structure (image produced from ⁵. (b) Honey-comb lattice structure. (c) Dark shiny appearance of graphene. b and c images produced from ⁶.

Graphene discovery has conceptually set in motion research in a new class of materials only singleatom thick, paving way for low-dimensional nano material research towards both fundamental



physics and potential applications in all walks of life. Graphene sheets when rolled become carbon nanotubes, and repacking a graphene sheet constitutes C_{60} as shown in Figure 1-1(a).

1.1 Low Dimensional Materials

There are many layered materials having bonded strongly in-plane and a weak inter-layer bonding. This weak interlayer bonding has been a seed of opportunity for scientist to harvest. This single layer of atoms is called 2D material. The nanostructured materials (NSMs) of same material can give different properties, thus stirred up interest in synthesis of materials of different dimensions and shapes. By definition NSMs are solid materials with at least one dimension in nanometers. A nanocrystal smaller than the exciton Bohr radius for that material, in at least one dimension, is said to be quantum confined⁷. A zero dimensional (0D) material has its excited electrons confined in all three dimensions, thus behaving as a quasi-atom with discrete energy states. The carbon nanotubes, silicon nanowires, ZnO nanorods, nanobelts and nanoribbons are examples of 1D materials with confinement in two dimensions. The density of states for 2D materials, exhibits a quasi-continuous step-like increase with increasing excitation energy. Currently, fabrication of 2D materials has gained significant attention because of the physicochemical characteristics that are unique when compared to bulk counterpart. These 2D materials have shown promise for successful application in nano electronic devices^{8–13}.

Currently, 2D materials family has come a long way from graphene being the sole member. The family now includes transition metal dichalcogenides (TMDs), and layered metal oxides. The chemical formula MX_2 , often trivially denotes the family with M a transition metal (Mo, Re, Nb, W, Ni or V) and X is a chalcogen (any of Se, Te, or S)^{14–17}.

1.2 Graphene

Graphene is the crystalline two-dimensional (2D) single atom thick sheet of carbon atoms arranged in a hexagonal lattice² as shown in Figure 1-1b). Graphene is transparent compared to dark charcoal black coloured graphite The simple yet effective work by Novoselov *et al.*² on electric field effect in graphene films was the first report on a stable isolated planar graphene. Figure 1-2 shows the patent trend for graphene from 2004 till the end of 2013¹⁸, thus emphasizing the ever growing interest in graphene in both industry and academia. It has been a revolutionary material due to its unique thermal ¹⁹, optoelectronic ²⁰, magnetic ²¹, superconducting ²² and



Graphene patents

Analysis of inventions involving the manufacture or application of graphene shows dramatic growth over the last decade from just 33 inventions described in published patents in 2004 to over 5,000 inventions last year



Figure 1-2: Graphene patent trend in a decade since the discovery.¹⁸

It has no band-gap with infinite Bohr radius that imparts metal-like conductivity in graphene sheets, thus possibility of confinement for any finite size of graphene²⁴. Figure 1-3 shows the historical timeline towards the discovery of exfoliated graphene by Geim et al.



*Figure 1-3: A timeline of selected events in the history of graphene for its preparation, isolation and characterization. Reproduced from Ref.*²⁵ *with permission from Wiley*



1.2.1 Synthesis of Graphene

Graphene synthesis is one among the most heavily researched issues. The search for the right method to get high quality large area graphene finds a major chunk of research in most cutting edge research laboratories. As a result many physical and chemical methods have surfaced for graphene synthesis, based on mechanical or chemical exfoliation of graphite²⁶, unzipping of CNTs²⁷, chemical reduction of GO²⁸, CVD or epitaxial growth^{29,30}

1.2.1.1 Mechanical Exfoliation

Dry mechanical exfoliation is the most popular as well as efficient methods for extracting single or few layer graphene³¹. The adhesive tape based process involves a simple graphite peeled off repeatedly and transferred on desired substrate² also called the scotch tape method. Although easy, yet time consuming process with low yield and low reproducibility at least cost. The process does not necessarily provide a good quality graphene, which may get damaged during repeated peel off steps.

1.2.1.2 Chemical exfoliation

A high-yield solution based process with better reproducibility at low cost is chemical exfoliation of graphite by Ultrasonication and intercalation before centrifugation^{26,32}. This involves use of inorganic salts such as (NH₄)₂SO₄, Na₂SO₄, K₂SO₄²⁶ or water-surfactant solution, sodium cholate³³. The centrifugation step segregates and helps select the largest graphene exfoliated during the exfoliation process. The ultrasonic cleavage is strongly dependent on the choice of solvents used with surfactants and ultrasonication parameters such as frequency, amplitude and time ³⁴.

1.2.1.3 GO reduction

Graphene oxide GO can be prepared by a number of routes. Graphene oxide synthesis by Hummers method involves graphite soaking in H₂SO₄ and KMnO₄. The resulting graphite oxide is sonicated to obtain single layer GO. Finally GO is reduced with suitable reagents to form reduced graphene oxide rGO^{35,36}. The use of acids renders structural defects on graphene. The graphene obtained by GO reduction is different from pristine graphene. This method is scalable, time efficient and economic despite reduction borne defects caused by reducing agents involved in the process.



CVD graphene



Figure 1-4: Schematic CVD based graphene synthesis. a) Deposition of copper thin film with native oxide; b) Exposure of Cu to CH4/H2 at 1000 oC; c) The graphene nucleated in previous step grow into graphene flakes. Reproduced from Ref.³⁷ with permission from The Royal Society of Chemistry

The schematic in Figure 1-4 illustrates typical CVD growth of graphene. CVD grown graphene is most efficient yet expensive method for graphene synthesis as it gives large surface areas with uniform and high crystal quality at large manufacturing volumes^{38,39}. Copper has least affinity and low carbon solubility compared to other transition metals (Co, Ni) towards carbon which makes it best candidate for catalyzing CVD graphene growth In a recent research up to 0.5 mm single crystal graphene was grown on copper foil by low pressure CVD⁴⁰.

1.2.2 Applications

Like any other material, graphene has been researched for applications in all walks of life, but compared to others, graphene has aced all the tests. Both public and private investments have fueled graphene research to graphene commercialization and applications. The market based products have already started to show up in form of tennis rackets, battery strap by Vorbeck, or the touch screen phones by Samsung. The size of graphene market has steadily increased alongside a surge in graphene patents as shown by Figure 1-2. In the meantime graphene applications classified by technological readiness as shown in, Figure 1-5 elaborate the current state and futuristic applications chalked out for graphene. Graphene is on the verge of being taken up as the material of choice for a variety of applications in futuristic products. In field of plasmonics, being ultimate thin 2D material, flexible and inert material which can be fabricated and patterned by standard silicon based clean room systems. Graphene plasmons are tunable either by electrostatic or chemical doping compared to noble metal counterparts⁴¹ and can extend into NIR and MIR regions of electromagnetic spectrum. Graphene sheets have been used to compose solid 3D networks of graphene sponges, foams, templates, and aerogels that exhibit high surface area, strength, accessible pore volume and high conductivity even at very low density⁴². Ahmad et al



reported graphene nanoribbon arrays for high performance NO₂ sensing applications down to 20 ppb.⁴³

Xu et al reported self-assembled 3D Graphene macrostructures for super capacitor applications ⁴⁴. Liquid phase exfoliated graphene as filler material in phase change materials has shown promise for thermal management of high power battery packs⁴⁵. Among other photonic applications, it has been used for solar cell ⁴⁶, graphene plasmonics⁴⁷, graphene based flexible OLEDs⁴⁸



*Figure 1-5: Graphene applications classified by technology readiness level. Reproduced from Ref*⁴¹ photodetector⁴⁹ and encapsulations⁵⁰. Graphene has also been used in super capacitors^{51,52}, energy storage^{53,54}, memory⁵⁵ and many other applications.



1.3 Graphene Quantum Dots

Band-gap induced graphene in form of graphene quantum dots (GQDs) and nanoribbons have found great interest in recent years. GQDs are the zero dimensional sp² hybridized carbon systems, same as the three dimensional (3D) structure in the case of graphite, 2D graphene, or onedimensional (1D) carbon nanotubes. Graphite is in fact multi-stacked graphene, whereas carbon nanotubes (CNTs) are graphene sheets rolled up into cylinders. GQDs are typically smaller than 20 nm in diameter ⁵⁶ comprised of single or multilayer graphene. GQDs have numerous proven applications in bio imaging ⁵⁷ and photonic devices such as solar cells ⁵⁸, photodetectors⁵⁹, light emitting diodes ⁶⁰ and many others due to the altered quantum confinement and presence of edge states⁶¹. GQDs are often synthesized by breaking materials comprised of 2D graphene sheets into 0D nanostructures. Compared with carbon dots, GQDs also have higher surface to volume ratio and the remarkable, graphene structure, hence providing them with comparatively superior properties.

1.3.1 Synthesis

There are various methods for GQD synthesis that can be divided into two major approaches: top down and bottom up approaches. Top down approaches involve the breaking down of mesoscopic 1D, 2D or 3D graphene structures such as CNTs, graphite, graphene platelets, carbon fibres or carbon black⁶² into smaller fragments by hydrothermal ⁶³, solvo-thermal ⁶⁴, electro-chemical ⁶⁵, microwave assisted hydrothermal ⁶⁶ or nanolithography ⁶⁷ to smaller fragments. The hydrothermal route involves separate graphene oxide cleaving and reduction processes, which require up to three days ⁶⁸. In bottom up approaches, such as hexaphenylbenzene pyrolysis ⁶⁹, tetra-phenyl-cyclo-pentadienones and diphenyl ether reflux under argon atmosphere ⁷⁰ or laser exposure of benzene ⁷¹ and compounds containing fullerenes like polycyclic aromatic molecules are used to synthesize GQDs. Most of these methods either use acids ^{72,73} or high temperature treatments during synthesis.

It is highly desirable to improve the synthesis of GQD with a desired size by minimizing the need for expensive equipment, high temperature process, and highly caustic and hazardous chemicals. In this study, a direct ultra-sonication assisted GQD synthesis approach was developed, which involves grinding and ultra-sonication in the presence of two solvents without the use of any acidic reagents ^{72,73}, high temperature treatments, or sophisticated apparatus ^{71,74}.



1.3.2 Applications

The graphene quantum dots were used as green and facile sensor for free chlorine in drinking water, thus envisioned for likely applications for drinking water ⁷⁵.

1.4 Graphene Nanocomposites

Nanostructured materials behave in an altogether different way when compared to their bulk counterparts. In last decade nanotechnology has become steadily richer for fabrication of nanostructures. The zero, one and two dimensional materials have been used to impart new properties for improving device characteristics, which were thought impossible in bulk form. Graphene has been the revolutionary two dimensional (2D) carbon containing benzene like repeating unit cell, with single atom thickness ⁵. It has proved to be revolutionary material for many novel applications such as photonics⁷⁶, plasmonics⁷⁷ and as transparent electrode material ⁷⁸. It has also been utilized in the form of composite with metallic nanoparticles ⁷⁹, nanowires ⁸⁰, and polymers for enhancing their shortcomings ⁸¹

1.5 Resistive Switching

The structure of a memristor is similar to a capacitor, as an insulating material is sandwiched between two metal electrodes as shown in Figure 1-6. This structure is also known as metal/insulator/metal (MIM) structure.



Figure 1-6: A schematic of sandwich structured resistive switching device.

There are many electrically induced resistive switching effects as illustrated in Figure 1-7. The nascent active layer in a memristor has a high resistance. An electrical voltage is applied to set off a process called electroforming voltage. Generally speaking, resistive switching in a memristor occurs on passage of suitable magnitude of electric current through thin film of an active material.



The resistance of the film can be switched back and forth, between a high resistance state (HRS), "OFF" state and a low resistance state (LRS), or "ON" state ⁸².



*Figure 1-7: Taxonometry of Resistive Switching memories (schematic adapted from*⁸³, *reproduced with permission).*

This switching is attributed to thermal, electronic and/or ionic effects ⁸⁴. The thickness of the active layer in the sandwich structure configuration has been reported from ~2 nm to several hundred nanometers. The core memristor attributes such as its conduction and switching mechanism, working speed, resistance ration in off and on states depend on the active layer. Ever since 1960s, researchers have explored various metal sulfide⁸⁵, organic^{86,87}, oxides of both metals⁸⁸ and non-metals⁸⁹ and phase change materials⁹⁰⁻⁹² as active layer. Many high dielectric constant polymers have been studied for memristive behavior. Poly(4-vinlyphenol) (PVP) finds application as dielectric in thin-film transistors (TFTs)^{93,94}, resistive switching and write-once read-many (WORM) memories ⁹⁵. Several polymer/nanoparticle ^{96,97} memory devices have been reported. The change in memristive behavior of PVP, in presence of graphene flakes, has been reported in our previous study⁹⁸.

1.5.1 Resistive switching mechanisms

The switching mechanism depends on the sandwich structure, the electrodes used as well as the mode of operation of the resistive switching device. The switching mechanisms can be classified



into five types. Ion migration, charge trapping/detrapping, thermochemical reaction, and the exclusive mechanisms for organic and inorganics. The ion migration mechanism requires an electroforming process whereby triggering stable RS behavior. The local rupture and reformation of the conducting filament (CF), reset and set the RS states, and switch between HRS and LRS. These ions can be cations or anions depending on the nature of electrodes and the active material used. The active electrode and the counter electrodes The CF are formed via electrochemical dissolution and latter redepositing active metal atoms. In case of Ag active electrode (AE) positive bias during electroforming and set steps undergo the anodic dissolution at the electrode, then migration of the Ag⁺ ions along grain boundaries towards counter electrode (CE) followed by the reduction of Ag⁺ ions and the growth of filaments. The device switches from HRS to LRS as soon as the filament short circuits the two device through the CF. The existing filament however does not sustain the joule heating along its thinner parts and ruptures the memory element back to the HRS. In 1976 Hirose⁹⁹ reported was first to study Ag/Ag-As₂S₃/ Mo sandwich structure and report the bipolar RS behavior. The range of materials reported until now has grown steadily to include oxides such as ZrO_2^{100} , $GO^{101,102}$ and $ZnO^{103,104}$, as well as polymers such as $PVP^{105,98,106}$, $P3HT^{107}$ and many others. The charge trapping/detrapping mechanism based memristors trap the charge carriers injected from electrodes in form of space charge. It then modulates the carrier injection and/or affect their flow through the active layer. The mechanism can be classified in three groups. The interfacial charge traps occur in case of Schottky junction formation that get altered by application of external electric fields. The charge traps may also occur within inorganic or organic active layers.in form of nanoparticles¹⁰⁸, graphene film⁵⁵ or quantum dots. The third category involves randomly distributed charge traps such as Au or Ag nanoparticles¹⁰⁹ carbon nanotubes¹¹⁰ or graphene flakes^{105,98}.In semiconductors memristors the forming and set processes occur by thermal decomposition of active layer and subsequently the CF formation whereas the reset process is caused by thermal meltdown of CFs formed during set process. The thermochemical reaction mechanism mediated RS has been reported for Pt/NiO/Pt¹¹¹ and Pt/ZnO/Pt¹¹². VO₂¹¹³ and NbOx¹¹⁴ studies have demonstrated insulator to metal transition from the field induced joule heating effect. The mechanism is responsible for nonvolatile RS behavior in VO2. The amorphous carbon RS studies has also shown similar process¹¹⁵. In case of polymers and organics the charge transfer as a consequence of external field is due to donor and acceptor moieties existing within



the active layers. For a PVK- C_{60} ¹¹⁶polymer based memristor charge transfer between carbazole and C_{60} demonstrated by 100:1 shows bipolar RS behavior upon sandwiching between Al and ITO.

1.5.2 Applications of memristor

Although mostly studied for memory applications, memristors have been studied for logic circuit, unconventional computing, spintronic, photo electronics, etc^{117–120}.

1.5.3 Types of Resistive Switching

In general, two switching schemes are prevalently reported and distinguished by the currentvoltage (I-V) characteristics as shown in Figure 1-8. A voltage polarity dependent process is known as unipolar, whereas its voltage polarity independent counterpart is known as bipolar switching.



*Figure 1-8: Unipolar and bipolar switching schemes. CC denotes compliance current, often needed to limit the ON current. (adapted from*⁸⁴, *reproduced with permission)*

In the case of bipolar switching the SET to ON and RESET to OFF states occur on opposite voltage polarities. In either of these characteristics, the reading voltage, V_{READ} to read the state is usually small so that it does not affect the state as shown in Figure 1-8.



Chapter 2. THIN FILM FABRICATION PROCESSES

2.1 Conventional Thin Film deposition Processes

Printed electronics industry now stands as a major contributor to human advancements and futuristic innovations such as 3D and 4D printing are already paving way for further advancement of human civilization. Printed electronics mostly deals with ability to print the whole device in batches so that costs are minimized with high end products into flexible and stretchable electronic applications. Thin film fabrication has been one most significant processes in nanotechnology. Conventional vacuum based processes employ various low to ultra-low vacuum for thin film fabrication. They include but are not limited to physical vapor deposition^{121,122}, chemical vapor deposition^{123–127}, sputtering^{128–130}, electron and ion-beam evaporation^{131–133}, molecular beam epitaxy¹³⁴⁻¹³⁷, laser ablation¹³⁸⁻¹⁴⁰, plasma¹⁴¹⁻¹⁴³ and atomic layer deposition^{144,145}. These processes have matured over the years and can give very high quality thin films of required materials over a variety of thicknesses. Recently the non-vacuum thin film fabrication technology has started showing promise. The advantages among others are low cost, less time consuming and batch process-ability. The solution processible colloids and inks market is on the move. The demand for room low temperature material processing is mainly driven by research for flexible and stretchable electronics. One method very common for solution processible thin film fabrication is spin coating, employed mostly for proof of concept devices. Processes such as dip coating, slot die coating, inkjet printing, electrohydrodynamic atomization^{60,146–151} have been employed for thin film fabrication at room temperature without the requirement of vacuum. The thin films fabricated by these techniques have been used in solar cells¹⁵², photodetectors¹⁵³, memristors¹⁰⁵, nanogenerators¹⁵⁴ and supercapacitors^{155–157}, which evidently proves, the viability of these techniques in printed electronics industry. The patterning of structures is just as important as thin film fabrication in an electronic device. Roll to Plate, screen, gravure, offset gravure, and electrohydrodynamic printing have been promising techniques that work efficiently well at room temperature and atmospheric pressure

2.2 Electrohydrodynamic Atomization (EHDA)

Electro-hydrodynamic atomization (EHDA) has been a preferred low-cost, non-contact, and efficient material printing technique that can be used in a variety of ways for layer by layer



deposition of materials. The details of EHDA phenomenon has been elaborated in detail by Poon ¹⁵⁸ and has been used for fabrication of thin film device applications^{159–162}, OLED⁶⁰, thin film memristor¹⁰⁵ and Schottky diodes¹⁶⁰. EHDA process optimization is carried out by carefully adjusting the operation parameters, such as the stand-off distance, operating voltage between the metallic nozzle with respect to grounding terminal, the diameter of nozzle, the conductivity and viscosity of the ink used, and the ink flow rate. The thickness of the thin films deposited by this process can be optimized either by varying number of passes or by controlling the speed of translation of needle across the target substrate. As in case of deposition of thin film deposition of Graphene nanoflake/PVP¹⁰⁵ nanocomposite by EHDA, the in-house built EHDA system^{147,159,163,164} fitted with a metallic capillary nozzle of a suitable inner diameter of 310 um (Havard 33G) was used. The schematic diagram of the EHDA setup is shown in Figure 2-1(a). The thin films were deposited on top of ITO as bottom electrode, and at the end finished up by a silver top electrode, as an ITO/active layer/Ag MIM sandwich structure schematically shown in Figure 2-1 (b). The prime requirement same as any other thin film deposition process, prior to EHDA of thin films, the as-received ITO coated PET substrates must be cleaned by acetone, ethanol and deionized water in sequence by bath sonication, for 5 min each, at room temperature and dried in air to get rid of any contaminants. Afterwards, the substrates were cleaned by ultraviolet-ozone exposure for 5 min and oxygen plasma treated for 3 min to get rid of organic residues from surface.



Figure 2-1: (a) Schematic configuration of an EHDA setup for deposition of composite for fabrication of thin films. (b) Device structure and voltage sweep applied across a typical device. The voltage across the two terminals is varied between +3 and -3 V.



Ink (PVP/Graphene nanocomposite in NMP) was fed to the liquid chamber via Teflon tube to nozzle by a syringe pump (Hamilton Model 1001 GASTIGHT syringe). The flow rate was controlled by using the pressure control system. An overall operating envelop for composite ink, was established by varying flow rates (50—900 μ l/h) and applied potentials plotted in Figure 2-2.



Figure 2-2: EHDA Operating envelop of Graphene/PVP composite. Insets (a)-(e) show the photographs of the jet in different modes. The stable cone jet mode is used for device fabrication

The optimum atomization mode called stable cone jet mode, was achieved at constant stand-off distance of 15 mm between the nozzle and the substrate. The different atomization modes of EHDA process were captured through high speed CCD camera (MotionPro X) Insets Figure 2-2 (a) to (e).

Initially at flow rate of 200 μ l/h, a dripping mode appeared from voltage of 3.3 to 3.7 kV. Further increasing voltage, micro-dripping appeared and continued until 4.2 kV. While increasing the voltage beyond 4.2 to 5.3 kV, ink emanated from nozzle in unstable cone jet mode. The desired atomization of ink in the stable cone-jet mode was achieved from 5.3 to 5.8 kV. Beyond 5.8 kV, the jet disintegrated, into the multi-jet mode. The substrate was placed on a computer controlled translation stage, and substrate movement was varied from 1 to 3 mm/s to establish ITO



coverage of about 1 x 2 cm² keeping stand-off distance fixed. The film was deposited at translation speed of 3 mm/s and cured at 120 °C for 6 hours in order to safeguard the PET from undergoing plastic deformation and evaporate solvent remnants.



Chapter 3. EXPERIMENTAL

3.1 Synthesis of Graphene Nanostructures and Nanocomposites

PVP (average M_W 25,000) powder, poly(melamine-co-formaldehyde), the cross-linking agent, propylene glycol methyl ether acetate and N-methyl-pyrrolidone (NMP), 1, 2-dichlorobenzene (DCB) solvent were purchased from Sigma Aldrich. Graphene flakes (less than four layers and surface area greater than 750 m²/g) were purchased from Cheap Tubes.

3.1.1 Graphene Flakes

Graphene flakes are graphene microstructures with lateral size in a few microns yet keeping single to few layer graphene in their structure.

3.1.2 Synthesis of Graphene Nanoflakes

The synthesis of a precursor solution for the EHDA is a critical benchmark for achieving the uniform thin film deposition ¹⁶⁵. Graphene nanoplatelets dispersion was synthesized¹⁴⁹ by dispersing graphene nanoplatelets (0.8 mg) in NMP (10 ml) by bath sonication for 15 min. This dispersion was subject to 2 hours of violent shaking by mechanical shaker and also 2 hours of stirring by a magnetic stirrer. To improve exfoliation and further the prospect of breaking the graphene flakes down to even smaller size ¹⁶⁶, the dispersion was bath sonicated for 6 hours at room temperature and probe sonicated for 1 hour. The ultrasonic energy transferred during this step increased temperature of water in the sonication bath. This rise in temperature was kept in check by consistent replacement of ice packs every 30 min. Only the top 75% supernatant in vile was extracted after centrifugation at 4000 RPM for 30 min. The supernatant was first filtered by using syringe filter (Whatman polypropylene) of pore size 0.45 μ m and later by another filter of pore size 0.2 μ m to remove any large size flakes remaining in solution. Thus lowering load and avoiding clogging of 0.2 μ m pore size filter by the larger flakes.

The Graphene flake size reduction was assessed by employing a Cs-corrected Spherical Aberration Correction Scanning TEM (JEM-ARM 200F), operated at 200kV. The success of the process is evident from the TEM analysis of these graphene flakes performed after sonication step. The processed graphene was analysed by TEM to confirm size reduction evidently shown in Figure 3-1 (a). The high resolution TEM image in Figure 3-1(a) shows that the flake size reduced to even smaller nanoflakes with average diameter of about 20 nm. Figure 3-1 (b) also reveals single to few



layer graphene, showing successful exfoliation of multilayer graphene flakes. Figure 3-2 shows synthesis of graphene nanoflakes from graphene flakes by processing steps mentioned above.



Figure 3-1: (a) High and (b) low magnification TEM images showing the nanosized graphene flakes after processing. The minimum size of these nanoflakes is ~20 nm.



Figure 3-2: Synthesis of Graphene nanoflakes from graphene flakes. The size of Graphene flakes as observed by TEM analyses



3.1.3 Synthesis of Graphene Quantum Dots

Graphene flakes were purchased from Cheap Tubes (44 layers and surface area 750 m²/g), and N-methyl-pyrrolidone (NMP) and 1, 2 dichlorobenzene (DCB) were purchased from Sigma Aldrich. All reagents were used as received without any further purification. First, 120 mg of graphene flakes and a few drops of NMP were poured in a mortar and then ground for 360 minutes using a pestle. The NMP was added to keep the powder moist to assist in grinding and exfoliation. The ground graphene was then dried at 100 °C for 1 h, and the powder was extracted and weighed for further processing. Next, 10 ml NMP, 10 ml DCB and 100 mg ground and dried powder were added in a cleaned bottle. The three were mixed thoroughly with a magnetic stirrer for 1 h at 1000 rpm at room temperature, followed by mechanical shaking for 4 h at 300 rpm. After mixing, the product was sonicated in an ice pack filled water bath for 4 h at room temperature. A Fisher Scientific Sonic Dismembrator (100 W) was used for ultra-sonication for 1 h to break the graphene sheets into even smaller fragments. The supernatant was collected after centrifugation for 30 min, using a WiseSpin (CF-10) at 5000, 8000, 10000 and 13500 rpm rotation speeds, named as samples S-1, S-2, S-3, and S-4, respectively. The supernatants were dried by evaporation of the solvents in air at 100°C. Finally, the dried GQD powder was used for XRD and Raman characterizations.

The GQDs were prepared by a sonication-assisted mechanical method. This approach involved first grinding few layer graphene flakes (GF) for 360 min in presence of N-Methy 1-2-pyrrolidone (NMP) to assist the exfoliation process, as well as to break down the flakes to smaller sizes ¹⁶⁷. Afterwards, violent shaking and ultra-sonication in the presence of 1,2 dichlorobenzene (DCB), in addition to NMP, helped further break down the graphene to even smaller fragments^{168,169}. DCB has already been reported as a good organic solvent for exfoliation of graphene¹⁷⁰. Figure 3-3 shows the flow diagram of the proposed chemo-mechanical process. The last stage of the process was centrifugation at high rotational speeds. Only supernatant species were collected for further characterization, since the sediments were expected to have a wide range of graphene sizes ¹⁷¹. The processed GF, collected before and after centrifugation have been shown in Figure 3-3. The colour change from opaque black to transparent golden yellow is clearly depicted in the photographs of the GQD sample before and after centrifugation. The flakes were broken down into smaller fragments during the chemo-mechanical treatment. The presence of NMP helped exfoliate the graphene, whereas the DCB was helpful for unzipping and the extraction of the GQDs.



Transmission electron microscopy (TEM) investigation of unprocessed graphene flake in Figure 3-4(a) shows few to single layer graphene sheet with long range crystalline order. whereas the selected area electron diffraction pattern in Figure 3-4(b) shows the characteristic crystalline graphene ^{172,173}. The surface scan histogram Figure 3-4(c) evaluated from the high resolution TEM image of GF shows lattice parameter of 0.24 nm in agreement with literature ¹⁷⁴. TEM



Figure 3-3 Schematic of Graphene QD synthesis by the co-solvent, grinding and ultra-sonicationassisted mechanical route. The graphene sheets were broken down initially by grinding, and then through ultra-sonication in the presence of NMP and DCB. The supernatant species extracted as a result of centrifugation showed a transparent golden yellow color compared to the opaque char black for chemo-mechanically processed graphene before centrifugation.

investigation was carried out for samples, S-1, S-2, S-3, and S-4, obtained after final centrifugation at 5000, 8000, 10000 and 13500 rpm respectively for 30 min as shown in Figure 3-5(a). Nano-Measurer 1.2 was used for calculating the diameters by analysing the TEM images of each sample. The insets show the high resolution TEM of individual GQDs from each of the four samples with lattice spacing of 0.24 nm and highly crystalline structure. 2D FFT pattern (shown in inset) of the TEM image of one of the GQD samples also validated the presence of the characteristic hexagonal graphene structure ¹⁷⁵. The average diameters of the GQDs collected after various centrifugation speeds have been tabulated in Table 1.





Figure 3-4: (a) HRTEM analysis of graphene flake in the sediment after processing shows crystallinity. b) The SAED pattern shows six-fold symmetry for graphene crystal structure, and c) the 2D surface scan profile shows the characteristic lattice spacing of about 0.24nm.

Table 1: Relation of centrifugation speed with diameters of extracted GQDs. It is evident from the table that variation in centrifugation speed yields change in the GQD diameters.

| Sample | Centrifugation Speed (rpm) | Diameter (nm) |
|------------|----------------------------|-----------------|
| S-1 | 5,000 | 3.92 ± 1.09 |
| S-2 | 8,000 | 3.04 ± 0.83 |
| S-3 | 10,000 | 2.62 ± 0.68 |
| S-4 | 13,500 | 1.96 ± 0.43 |



The size distribution of the extracted GQDs obtained by variation of centrifugation speed is shown in Figure 3-5(b). Diameters of 200 GQDs spotted from TEM images were calculated for each sample. The average size of GQD in S-1 was 3.92 ± 1.09 nm, the largest among the 4 samples. The average GQD diameters for S-2 and S-3 were 3.04 ± 0.83 nm and 2.62 ± 0.68 nm, respectively. Sample S-4 had the smallest GQDs among the four, with an average size of 1.96 ± 0.43 nm, and also showed the narrowest distribution compared to the samples centrifuged at lower speeds. All samples accorded well with Gaussian distribution. The change in diameters of the extracted GQDs showed an inverse relation with the centrifugation speed, as shown in Figure 3-6. Thus, it was inferred that it is possible to extract GQDs of the desired size from the supernatant by controlling the centrifugation speed ¹⁶⁶



Figure 3-5: a) TEM images of GQDs extracted at different centrifugation speeds: S-1 at 5000, S-2 at 8000, S-3 at 10000 and S-4 at 13500 rpm. The insets show respective HRTEM images of the GQDs from each sample, showing 0.24 nm lattice spacing. The 2D FFT of the GQD TEM image also showed the six fold rotation symmetry for the graphene lattice, and also provides evidence of crystallinity. b) The diameter distribution of the extracted GQDs samples S-1 through S-4. Diameter distribution in each sample agrees with Gaussian distribution. The average QD size in each GQD sample decreases appreciably, from about 4 nm @ 5000 rpm to about 2 nm @ 13500 rpm centrifugation, performed for the 30 min for each sample.




Figure 3-6: The change in GQD diameter with centrifugation speed is plotted, showing a decrease in average size and increase in homogeneity of the GQDs extracted at high centrifugation speeds. The average GQD size in the case of the fastest centrifugation of 13500 rpm

The solvents from sample S-3 were evaporated and GQD powder was collected for XRD and Raman spectral analyses, to confirm the quality of as-prepared GQDs. The Raman spectra Figure 3-7(a) of GQDs reveal the existence of the D band at 1353 cm⁻¹ corresponding to the disorder-induced phonon mode. This band involves iTO phonon around the K-point like the 2D band¹⁷⁶. The G band at 1591 cm⁻¹ corresponding to Raman-allowed E_{2g} phonon mode of vibrations was observed due to existence of graphitic structures^{71,177}. The ratio of peak intensities for the D and G bands I_D/I_G of graphene-containing species determines the graphene crystallinity. The I_D/I_G determined from the Raman peaks was about 0.88, Raman spectroscopy confirms the quality of the as-prepared GQD in powder sample ^{178,179}. A typical X-ray diffraction (XRD) profile for synthesized GQD is shown in Figure 3-7(b). The 20 diffraction peak at 25° corresponded to the 002 peak of graphitic structure. The diffraction peak was in the range of 21° to 29°. The size and



orientation of the GQDs in the powder sample increased the peak width. The d spacing for the GQDs powder was calculated to be 3.4 Å, using the relation $d \sin \theta = n\lambda$, was in good agreement with the reported values (3.40-4.81)⁷³.



Figure 3-7: a) The Raman spectrum of GQD powder extracted after evaporating the solvents from the GQDs of sample S-3 at 100 °C. The ratio of the intensities of the D band at 1321 cm⁻¹ and the G band at 1590 cm⁻¹ was about 0.73, which provides evidence of crystallinity within the GQD graphene phases. b) The XRD profile of GQD powder extracted after drying out the NMP and DCB shows a wide 002 peak at around 25°. c) Comparison of the FTIR of NMP, DCB and GQD powder sample shows complete evaporation of the solvents. The absence of a strong peak at 1742 cm⁻¹ from NMP and at 1358 cm⁻¹ from DCB when compared with the FTIR spectrum of GQD powder suggests the removal of both solvents inform the powder sample.

Here, in the formula 'n' is the diffraction order taken as 1 for first order diffraction, and λ = 1.54 nm for Cu K_a line. Figure 3-7(c) summarizes the Fourier transform infrared (FTIR) spectroscopy of the GQDs in sample S-3, DCB and NMP, respectively to explore the bonding composition. DCB showed absorption peaks at 578 cm⁻¹ for C-Cl stretching, 1066 cm⁻¹ for C-O-C stretching, and 1358 cm⁻¹ for C-H rocking due to alkanes. The IR spectrum of NMP showed the



characteristic strong carbonyl peak at 1742 cm⁻¹, which was absent in the GQD samples. Comparison with the FTIR spectrum of GQD showed almost complete evaporation of the solvents during powder extraction. An obvious absorption peak cantered at 1674 cm^{-1 56} was caused by C=C stretching of aromatic rings ⁷¹. The broad peak cantered at 3429 cm⁻¹ indicates the presence of hydroxyl species in the sample, which might assist with the solubility of GQDs in water ¹⁸⁰. The peaks at 1380 cm⁻¹ and 2924 cm⁻¹ showed the presence of C-H on the surface of the GQDs ⁷¹, presumably originating from alkyl groups. The 1262 cm⁻¹ peak was attributed to the presence of C-O-C.



Figure 3-8: a) The UV absorbance spectrum of GQD sample showed strong absorption in the UV region of the electromagnetic spectrum. The absorption subsided in the visible spectrum. b) The energy bandgap calculated by Tauc plot for GQD showed x intercept at 2.6 eV. c) PL properties of the GQDs. The PL spectrum shows twin peaks with strongest emission at 350 nm excitation wavelength compared to 300 nm through 350 nm. d) The GQDs in golden yellow ambient light show blue luminescence for a 365 nm UV lamp illumination. The same sample heated for 3 h at 60 C changes to pale yellow in ambient light and greenish cyan for 365 nm excitation



The UV visible absorption spectroscopy performed on the GQDs revealed weak shoulders at 322 nm and 364 nm ¹⁸¹, as shown in Figure 3-8(a). The GQDs show a broad UV–vis absorption band with a strong shoulder at 289 nm ¹⁸² corresponding to $n \rightarrow \pi^*$ transition due to C=O, whereas a hump at 363 nm.

These results are comparable to those obtained for samples prepared by the hydrothermal approach ⁶⁸. UV absorption spectra were very difficult to compensate for strong absorption of NMP for wavelengths <265 nm. Therefore the UV absorption wavelengths below 265 nm are not discussed here. The bandgap for GQDs evaluated by Tauc plot as shown in Figure 3-8(b) was about 2.6 eV, which shows promise for application of the GQDs in optoelectronics. The photoluminescence (PL) spectrum was obtained by exciting the GQD sample by a series of excitation wavelengths (λ_{ex}) from 300 to 350 nm as shown in Figure 3-8(c). The PL response for less than 300 nm excitation wavelengths is very low compared to that for 350 nm > λ_{ex} > 300 nm. The PL emission was recorded in form of twin peaks at 442 nm (blue), and 493 nm (cyan) which steadily increased in PL intensity with λ_{ex} without shift. For most values of λ_{ex} , the intensity of 442 nm peak is less than that of 493 nm PL emission peak except for 310 – 330 nm excitation wavelengths. The GQDs excited at 350 nm showed most intense photoluminescence. All asprepared GQD samples were pale yellow under ambient light and showed blue luminescence when illuminated by a 365 nm UV as photographed in Figure 3-8(d). The same sample when heated at 60 C for 3 hours on hot plate, changed its colour from pale yellow to a golden yellow and showed cyan instead of blue luminescence when exposed to a 365 nm UV lamp, due to agglomeration of the quantum particles. The approach presented here is scalable, offers a high yield of ~7 mg/ml and low cost synthesis of GQDs of desired size. The study also describes the significance and compatibility of the approach for solution process technologies to integrate the as synthesized GQDs in printed electronic devices, without addition of new solvents that may harm quality of GQDs. Here the solution processible GQDs embedded polymer thin films have been printed using electrohydrodynamic atomization (EHDA)¹⁰⁵ and employed in all printed flexible array of resistive switching devices. The successful integration of the approach with solution processible printing can assist future development of other GQDs based, thin film electronic and optoelectronic devices.



3.1.4 Synthesis of Graphene PVP Nanocomposite

PVP ink was synthesized by using previously reported ink synthesis methodology¹⁶³. The viscosity of the ink was measured to be 27.7 mPa using a VM-10A viscometer. The graphene flakes were dispersed in 4 ml NMP solvent, bath sonicated for 30 min at room temperature and centrifugation was performed for 30 min at 3000 rpm. ¹⁸³ The supernatant was collected from the graphene flake dispersion. For the synthesis of PVP/graphene blended dispersion, same weight percentage of PVP powder as used to synthesize the PVP ink was added to this graphene supernatant. The viscosity of this blended dispersion was measured to be 22.5 mPa.

3.1.5 Synthesis of Graphene Nanoflake/PVP Nanocomposite

PVP powder (10% wt.) and poly (melamine-co-formaldehyde 2%) as cross-linker were added to the filtered graphene nanoflakes in NMP. The mixture was then thoroughly mixed by employing bath sonication for 30 min, probe sonication for 10 min and magnetic stirring for further 24 hours. All steps were carried out at room temperature. The viscosity of the graphene/PVP nanocomposite in NMP was measured to be 19.5 mPa using a VM-10A viscometer.

3.1.6 Synthesis of Precursor for GQD/PVP Nanocomposite Memristor

A small quantity of Poly 4-Vinyl Phenol (PVP) in ratio 2:1 with GQDs was added to the GQD dispersion in NMP/DCB to synthesize the solution processible GQD/PVP nanocomposite. For uniform dispersion of PVP:GQD, bath sonication was used for 10 min and ultrasonication for 5 min.



3.2 Resistive switching in Graphene nanostructure composites

Various memristive devices were fabricated to explore their respective resistive switching characteristics. The active layers were realized by suitable composites of polymer with either Graphene flakes, Graphene Nanoflakes or GQDs.

3.2.1 RS in Graphene flake PVP composites

For the first polymer composite, Graphene flakes were used with polymer PVP. In this case three approaches were used whereby each memristive device was fabricated in ITO/active layer/Ag MIM sandwich structure as shown in Figure 3-9.



Figure 3-9: Schematic showing MIM structure for memristive device with the block diagram. The silver dot acts as top electrode whereas ITO acts as bottom electrode for sandwiched MIM memristive device.



Figure 3-10: Schematic showing active layer within devices fabricated with LBL, SB, and blended approaches.

The active layers of devices have been summarized in Figure 3-10, differentiating the effect of three approaches on active layer formation. The schematic shown in Figure 3-11 summarizes the active layer fabrication process for the three approaches. In the first approach called layer-by-



layer (LBL) approach, PVP was spin coated on ITO coated glass at 1500 rpm and annealed at 100 °C for 1 h. Afterwards graphene dispersion was spin coated at 1500 rpm over the annealed PVP film and annealed at 100 °C for 6 h. For the semi-blend (SB) approach PVP film was spin coated on ITO coated glass and graphene dispersion was spin coated on it without annealing the PVP film.



Figure 3-11: Spin coating the thin films of graphene and PVP using (a) LBL, (b) SB, and (c) blended approaches on ITO coated glass. The top electrode in each case is a silver dot less than 1 mm² diameter.

The films were annealed at 100 °C for 6 h after spin coating the two materials. The temperature was kept at 100 °C to safeguard the PVP film integrity. Blended PVP-graphene composite film was fabricated by spin coating PVP/graphene flakes dispersion on ITO coated glass substrate at 1500 rpm. The film was then annealed at 100 °C for 6 h. The LBL approach rendered a definite interface between PVP and graphene film on top of the PVP film. The films fabricated by SB approach lacks such structure because graphene flakes intrude the PVP near the interface. This decreases the individual thickness of the pure PVP and graphene films and PVP-graphene composite was formed at the interface between the two layers.

For each of these devices a dot of high conductivity silver paste of average area of less than 1 mm² was used as top electrode while ITO served as bottom electrode to complete MIM



memristive device. The double voltage sweep was applied across the two electrodes to study current-voltage characteristics of the devices.



3.2.1.1 Morphological analysis

Figure 3-12: SEM images at low and high magnifications showing uniform surface characteristics for devices fabricated with LBL (a, b), SB (c, d), and blended (e, f) approaches.



Film thickness was measured by a non-destructive, thin film-thickness machine, K-MAC ST4000-DLX, based on interference spectrum of white light incident on the film surface. For each film, the thickness measurement was averaged after taking at least seven readings at different locations in such a way that the measurement represents almost all the area of sample. The results reveal that the active layers fabricated by LBL, SB and blended approaches were of good uniformity with average thicknesses of $\sim 210\pm 16$, $\sim 200\pm 13$, and $\sim 170\pm 11$ nm, respectively.

The surface morphology plays a crucial role in performance of thin film devices. SEM analysis was performed for films fabricated by each approach (Figure 3-12). The films fabricated by LBL approach (a, b), SB approach (c, d), and blended approach (e, f) showed no cracks across films' surfaces and appreciably uniform surface morphology.

3.2.1.2 Electrical characterization



Figure 3-13: Forming of active layer occurred at just below 0.8 V at low compliance current of 1 μ A. The resistance of the film drops and device is said be turned on from low current off state. The resistance has stepped down from R_{OFF} to R_{ON}

For the electrical characterization, I–V characteristics of the ITO/active material/Ag device were analysed by using the top Ag electrode connected to the driving voltage whereas keeping bottom ITO electrode grounded. The characterization was performed by initially applying a forming voltage, which is necessary for stable resistive switching operation ^{82,184,102}. The



electroforming process was performed by varying applied voltage from 0 to 2 V with compliance current (CC) of 1 μ A on one of the devices as shown in Figure 3-13.

Three sets of identical devices were fabricated using each approach. The conventional I-V characteristics at 100 μ A CC for 20 double voltage sweeps applied from +2 to -2 V are shown in Figure 3-14.



Figure 3-14: I-V characteristics of devices fabricated by blended approach. The memristive device characterized at compliance current of 100 μ A. The devices fabricated by this approach were more robust as evident by number of voltage sweeps in legend

The blended layer devices performed appreciably showing stable switching during voltage sweeps. The voltage was first increased from 0 to 2 V keeping 100 μ A CC to avoid hard breakdown of devices due to overflow of current. The low current allowed by the device was termed as the HRS for the device. Upon decreasing the voltage back from 2 to 0 V, the current did not decrease but device allowed more current to flow through it. This state was defined as the LRS since it showed lower resistance to the flow of current as shown by the changed slope of I-V curve when voltage was decreasing from 2V to 0V. This state transition can be clearly seen to have occurred at 0.8 V. When the voltage was gradually decreased from 0 to -2 V, Schottky behaviour became prominent, the current reached and remained constrained to 100 μ A beyond -1.2 V. The voltage was then changed from -2 to 0 V gradually but this time the device switched from this HRS to LRS at around -0.9 V again, thereby completing the pinched hysteresis loop. During voltage sweep



between -2 and 2 V, the I-V plot was not symmetric across 0 V line. However, repeated voltage sweeps, show both the retentivity and stability of the blended layer device are better than the devices fabricated by other two approaches. The blended layer devices sustained more than 20 double voltage sweeps whereas the other devices were not able to sustain the repeated voltage sweeps. Figure 3-15(a) shows the semi log I-V characteristics of blended memristive device at 100 μ A CC, each voltage sweep carried out at 60 s interval between voltages of 2 and -2 V. The endurance of the fabricated device was tested by applying multiple voltage sweeps. Figure 3-15 (b) shows the endurance analysis of the fabricated device at a reading voltage (V_{READ}) of around 0.4 V. Figure 3-15 (b) shows a consistent OFF/ON ratio of more than 5.



Figure 3-15: (a) Semilog I-V characteristics of blended memristive device at 100 μ A CC, each voltage sweep carried out at 60 s interval between 2 and -2 V. (b) Resistance vs switching-cycle graph indicating ON/OFF ratio of roughly 5:1. The reading voltage was around 0.4 V for the measurements. The device performed appreciably up to 20 voltage sweeps.

The average resistance during the OFF state remained about 14 k Ω and decreased to about 4 k Ω during the ON state. Resistance variation of the HRS and LRS for the same device was evaluated. R_{OFF}/R_{ON} was observed to be approximately 5 when read at V_{READ}=0.4 V. The IV characteristics for devices fabricated using each blended, LBL and Semi blend approaches at 5





Figure 3-16: First double voltage I-V sweep for the devices at 5 mA CC fabricated by blended, SB, and LBL approaches. Most devices fabricated by SB and LBL approaches did not withstand 10 voltage sweeps, and the memristive behavior was unstable after 10th voltage sweep. Only blended device was able to retain switching characteristics for more than 20 voltage sweeps without failing.

1

2

0

Voltage (V)

-1

-6.0m

-2



Figure 3-17: (a) Semilog I-V characteristics of LBL memristive device at 100 µA CC, each voltage sweep carried out at 60 s interval between 2 and -2 V.(b) Resistance vs switching-cycle graph indicating ON/OFF ratio for the LBL fabricated device.



The devices in each of these cases showed memristive behavior initially for first few voltage sweeps. However, only the blended devices were able to withstand and retain switching behavior for more than 20 voltage sweeps. The LBL and semi blend devices could not repeat the switching behavior beyond 10 voltage sweeps. Figure 3-17 shows semi-log I-V characteristic for 10 double voltage sweeps performed at 100 µA CC.

The endurance test on this type of device in Figure 3-17 shows that the device's HRS and LRS values were not uniform but changed during first 10 cycles. For a batch of 3 devices tested for each approach, no LBL device withstood more than 11 voltage sweeps even at 100 μ A CC.



Figure 3-18) (a) Semilog I-V characteristics of SB memristive device at 100 μ A CC, each voltage sweep carried out at 60 s interval between 2 and -2 V. (b) Resistance vs switching-cycle graph indicating ON/OFF ratio was lower and non-uniform compared to blended device. The reading voltage was around 0.4 V for the measurements

The device fabricated by SB approach showed even worse endurance and unstable resistive switching as shown in Figure 3-18.

The device initially showed a high HRS of about 20 k Ω , which kept decreasing until the device got hard breakdown. SB memristive devices had an unstable LRS which varied between 6 to 4 k Ω . The R_{OFF}/ R_{ON} for LBL and SB devices were calculated to be just above 3 at 100 μ A CC for same time and voltages as done for blended active layer based devices.

The blended approach yielded most stable memristive behavior among the three approaches. The performance of the memristive devices based on SB and LBL approaches lacked stability at 5 mA CC. The reason for better memristive behavior in blended device is proposed to be due to the formation of composite structure and thus improvement in uniformity within the active layer. The graphene flakes get embedded within the PVP polymer matrix and thus enhance the charge



trap densities within the device active layer. Since the memristance behavior is enhanced for thinner insulating films, this factor can be attributed as the reason for comparatively stable performance of the blended structure memristive device. Considering I-V characterizations, the blended approach showed better memristive behavior compared to SB and LBL approaches. The graphene flakes in case of LBL approach were not embedded within the PVP polymer matrix but formed a conducting layer on top of the highly insulating PVP films. The cross section of films fabricated by SB approach had three different. The top part of conducting graphene flakes, the bottom part of PVP only while the middle one becomes a PVP-graphene composite. The memristive behavior scales with the insulating layer thickness. The device based on SB approach showed unstable and low endurance memristive behavior due to lack of uniformity and variation of materials across the two electrodes. This non-repeatability was due to formation of simultaneous multiple conducting filaments within the active layer. The graphene oxide memristor fabricated by EHDA ¹⁰² technique also reported similar ON/OFF ratio.



3.2.2 Graphene/PVP nanocomposite based flexible memristor

3.2.2.1 Device fabrication

The conjet and atomization of Graphene nanoflake/PVP nanocomposite ink has already been explain as introduction to EHDA section. The deposited thin films were sandwiched between ITO and Ag top electrode. A small dot of high conductivity Ag paste, of an average area ~1 to 3 mm² was used as top electrode for all devices. Whereas ITO served as bottom electrode to complete MIM memristive device. Double voltage sweeps were applied across the two electrodes, to study current-voltage characteristics of the devices.

3.2.2.2 Morphological analyses

For each thin film used as an active layer, the thickness was measured by taking average of at least 7 measurements at different locations of film, repeated one after the other, in such a way that the set of measurements represented almost the whole top surface of the sample. The results reveal that the active layers fabricated by EHDA show good uniformity with average thicknesses of ~140 \pm 7 nm.

Surface morphology of printed nanocomposite film on ITO coated PET substrate were analysed using 3D Nanomap surface analyser and FESEM. The surface profiler with nanoscale accuracy was used for surface roughness measurement in phase shifting interferometry (PSI) mode. The 2D, X-direction, and 3D surface profiles of graphene/PVP nanocomposite thin film have been shown in Figure 3-19(a) to (c) respectively. The average roughness observed for the film is 2.59 nm, while the film linear variation in roughness along x axis is about 9.47 nm. Figure 3-19 (d) and (e) show low and high resolution FESEM images of deposited graphene/PVP nanocomposite respectively. Thus FESEM images reveal uniformity of films, devoid of cracks, voids or humps at nano-scale. This attribute plays important role for reliable working of thin film, employed in electronic devices. The inset in Figure 3-19(e) shows the FIB image of the nanocomposite thin film deposited on top of ITO coated PET substrate. The observed thickness of the thin film was about 150 nm in close agreement with thickness profiler data. The Pt coating was deposited to protect the film from possible damage during sample preparation.





Figure 3-19: Morphological analysis of thin film using 3D Nanomap for (a) two-dimensional map film roughness, (b) linear roughness plot along x-axis, and (c) three-dimensional map for the film surface shows the occasional protrusion of graphene flakes in PVP thin film. (d) Low magnification scale bar =1 μ m (e) high magnification scale bar =300 nm SEM micrographs also show uniformly deposited composite thin films by EHDA. The Inset shows the FIB cross sectional image of the nanocomposite film deposited on top of ITO coated PET substrate

3.2.2.3 Optical characterization

The optical transmittance of thin films collected by UV/VIS/NIR Spectrophotometer, shows that the composite thin films were optically transparent in visible and near infra-red region (NIR), showing promise for use in optoelectronic applications as shown in Figure 3-20. The composite film showed negligible transmittance in UV region below 288 nm, and a sharp increase in transmittance with gradual increase in incident wavelength from 288 to 377 nm. The transmittance remained about ~87 % beyond 377 nm in visible and NIR regions. FT-IR analysis [Bruker 66/S, Germany] for functional groups in graphene nanoflakes after ultra-sonication has been shown in Figure 3-21. The peak at ~3436 cm⁻¹ is attributed to –OH, whereas 2926 and 2852 cm⁻¹ represent the C-H stretch vibrations ¹⁸⁵. The absence of characteristic graphene oxide peaks shows that the processing in presence of NMP does not induce any oxidation in graphene flakes. NMP assisted exfoliation and break down of the graphene flakes into even smaller parts has been evidently



shown in TEM images



Figure 3-20: Transmittance spectrum of composite film shows up to 87% transmittance in visible and near-infrared regions. The films show strong absorbance in ultraviolet region.



Figure 3-21: FTIR spectrum of graphene nanoflakes after processing by stirring, shaking and ultrasonication.



3.2.2.4 Electrical characterization

The electrical properties of the fabricated Ag/organic nanocomposite/ITO device were further analysed by resistive switching studies. For current–voltage (I–V) characteristics the ITO/active material/Ag device was biased by connecting Ag, the top electrode to driving voltage whereas ITO, the bottom electrode was grounded. The characterization was performed by initially applying a forming voltage, which is necessary for stable resistive switching operation ^{82,184,102} as shown in Figure 3-22. The electroforming process was performed by varying applied voltage from 0 V to 7 V, with compliance current (CC) of 100 μ A. Initially device started in HRS and switched



Figure 3-22: Electroforming of Graphene/PVP nanocomposite film as memristor performed by slowly increasing potential across composite film as an active layer in Ag/active layer/ITO memristor device

After performing the electroforming process, double voltage sweeps between extremes of -3 and +3V were used to analyse the resistive switching behaviour of device. The magnitude of applied voltage is fairly small compared to other printed electronic devices¹⁸⁶. The voltage sweeps was applied repeatedly to evaluate memristor behaviour at low compliance currents (CC) of 10



nA. The conventional Semilog I-V characteristics at 10 nA CC, for one of the double voltage sweeps applied between -3 and +3V is shown in Figure 3-23.



Figure 3-23: Semilog I-V characteristic of Nanocomposite film used as a memristor at 10 nA compliance current

The devices exhibited remarkable stable resistive switching behaviour, by sustaining the resistive states, for repeated voltage sweeps. Figure 3-24 shows the device robustness for resistive states in the device, on application of multiple voltage sweeps. The voltage was first increased from 0 V to 3 V keeping 10 nA CC to avoid hard breakdown of devices due to overflow of current. The low current allowed by the device was termed as the HRS for the device. The device switched to high current state, called LRS after about 2V. However, decreasing the voltage from 3 to 0 V, the current did not decrease as the high current continued to flow through the device, until about 0.8 V, after which the current started to decrease and reached zero at 0 V. This state was defined as the LRS, since it showed lower resistance to the flow of current as shown by the changed slope



of I-V curve when voltage was decreasing from 3 V to 0 V. This state transition can be clearly seen to have occurred at 0.8 V.



Figure 3-24: The endurance test performed by applying multiple voltage sweeps between -3 and 3 V at VREAD =0.5V

When the voltage was gradually decreased from 0 to -3V, Schottky behaviour became prominent, the current reached and remained constrained to 10 nA beyond -1.2 V. Changing applied voltage from -3 to 0 V, device showed change in resistance state at about -2.5 V and finally showed 0 A at 0 V, hence completing the characteristic hysteresis loop.

The devices showed stable memristive behaviour due to the Ag conducting filament formation and rupture within PVP matrix. However the resistive switching is further stabilized due to charge trapping within highly conductive nanographene in composite thin film^{169,187,188}. The graphene nanoflakes embedded within the PVP polymer matrix, enhance the charge trap densities within the device active layer. Since the memristance behaviour is enhanced for thinner insulating films, this factor can be attributed as the reason for stable performance of the Graphene/PVP nanocomposite memristive device. More than 80% devices in our current study, with sandwiched composite films used as resistive switching devices showed consistent switching behaviour for



endurance testing, retention time, and flexibility. The Values of LRS and HRS were appreciably stable. The average value of HRS read at 0.5 V was about $1.55 \times 10^9 \Omega$ and $5 \times 10^7 \Omega$ for the LRS after 40 consecutive voltage sweeps between -3 and +3 V. The average R_{OFF}: R_{ON} remained consistently around 35:1, which is vital for a memory device. The retention time of the fabricated memristor was measured to be over 1 hour as indicated in Figure 3-25 and are expected to retain the resistive states for longer periods as well.



Figure 3-25: Device retentivity of over 1 hour measured for the Graphene/PVP composite memristor measured at 0.5 V.

PET has served as a good substrate for fabricating flexible electronic devices. The device's ability to retain its function when flexed, shows its feasibility for flexible electronics applications. The flexibility of the devices fabricated on PET substrates was examined by flexing devices from flat Figure 3-26(a) to a semi-circle Figure 3-26 (b) repeatedly for at least 50 flexes by wrapping them around cylindrical rods Figure 3-26(c). The slight variation in the HRS and LRS of device when bent to various diameters from 3.5 cm to 1.5 cm, Figure 3-26(d) shows flexibility of the device at varied bending radii.





Figure 3-26: Evaluation of flexibility of the fabricated device. The devices are flexed between (a) flat to (b) a semi-circle for 50 continuous flexes by wrapping around cylinders of different diameters. (c)Devices wrapped around a cylinder with 2.5 cm diameter. (d) The variation in device's states when wrapped around cylinders of various diameters from 3.5 cm to a minimum of 1.5 cm

The devices bent beyond 1.8 cm, became open circuit due to silver electrode cracking and breakdown. This limit could be lowered further for even shorter bending radii if top electrode had been more flexible. Stable HRS and LRS in these devices, show their potential for flexible resistive switching applications.



3.2.3 All printed Poly(4-vinylphenol)–graphene quantum dot based memristor

The solution processible GQDs based nanocomposite with PVP was used for fabrication of PVP/GQD thin film memristor. For the fabrication of a flexible GQD–polymer nanocomposite memory device, mass printing technologies, such as EHDA and roll to plate were employed. A 2x2 array of 4 resistive switching devices was fabricated with insulating polymer (PVP) used as host material for the GQDs. The top and cross section views of the device has been schematically shown in Figure 3-27(a). PET substrates were cleaned using ethyl alcohol, deionized water and the exposed to UV light for 5 minutes. The development of the patterned Ag top and bottom electrodes was carried out employing roll to plate technology. Here 11 kgF force waste applied for pattern transfer with a delay time of 3 sec. Fine ~250 μ m wide Ag patterns were achieved after sintering the samples at 100 °C for 1 h as shown by optical microscope micrograph in Figure 3-27(b).



Figure 3-27: Schematic diagram all printed GQD based flexible resistive switching 2x2 array top view [top left] and cross section view [bottom left]. b) The optical microscope image showing the cross points of Ag electrodes sandwiching the solution processed transparent GQD embedded polymer active layer

Prior to deposition of active layer, part of Ag bottom electrodes as columns was carefully masked by tape for electrical connections. The thin film of nanocomposite as active material (~100 nm) was deposited using EHDA setup. After curing the deposited thin films at 100 °C for 2 h, rows of top electrodes were deposited on top of active layer again by roll to plate technology. The top and bottom electrodes crossed each other at 4 locations with active layer sandwiched between them, hence forming memristors. High Figure 3-28(a) and low Figure 3-28(b) resolution FESEM images of nanocomposite thin films evidently prove the uniformity of the thin film. The thickness



measurement was averaged after taking at least seven readings at different locations in such a way that the measurement represents almost all the area of thin film surface. The calculated thickness of thin film was 120 ± 19 nm.



Figure 3-28: a) High resolution b) low resolution FESEM images. c) First I-V and d) Semilog I-V plots for a memristor. e) The comparison of I-V and f) Semilog characteristics of four memristors of the 2x2 memristor array



In order to study the electrical properties of the as-fabricated $2x^2$ array of 4 resistive switching devices, current-voltage (I-V) measurements were performed at room temperature by using a semiconductor characterization system. For each of the as-fabricated Ag/PVP: GQD/Ag memristor, the bottom Ag electrode was grounded whereas the bias was applied to the top Ag electrode. The voltage stress was applied on the highly resistive pristine device (>10³ Ω), in a sequence of 0 V to +1 V to 0 V. By steady increase of the voltages imposed on individual device, a pronounced change of resistance from the pristine high resistance state (HRS) to the relative low resistance state (LRS) is observed at a SET voltage of about 0.65 V. After the transition from HRS (~170 Ω) to LRS (~28 Ω) the device remained in LRS, for the subsequent sweep from +1V to 0 V. The device shows no current when voltage drops to 0V. During steady voltage transition 0 V to -1 V to 0 V, the devices keep steady LRS state. The resistance steadily increases with increase in magnitude of negative voltage until it reaches RESET voltage, V_{max} = -0.69 V. Beyond V_{max} , the current decreases rapidly, switching device OFF to the HRS from LRS state as shown in Figure 3-28(c). Figure 3-28(d) elaborates the semi-log I-V response of the same memristor. Figure 36e and 36f compare the I-V and semi-log I-V responses for each of the four devices. The four devices show negligible change in memristor parameters. The robustness of the array was tested by applying voltage stress cycles on each of the four devices. The voltage stress applied on the devices showed stable switching for 100 repeated voltage stresses Figure 3-29(a) and retentivity of over a day as shown in Figure 3-29(b). The striking difference for the GQD embedded PVP based memristor with other PVP based memristors fabricated in past was the ability to withstand higher currents without breakdown, despite operating at more than 50 mA compliance currents. The very small variation in resistance states for memristor M1 dictates stability of the device over large number of voltage cycles as well as time and showed stability for over well 24 h. To evaluate flexibility of the PET based memristor array, it was flexed for over 200 times, yet the device remained undeterred for its memristor function as shown in Figure 3-29(c) whereas the devices had remarkable performance for repeated bending cycles. The minimum bending diameter of 1.5 cm was observed as stable limit for device to function without breakdown. The device broke down





Figure 3-29: The resistive switching characteristics of the devices. a) The voltage stress cycles b) c) retentivity and d) flexibility tests performed for the memristors at V_{Read} = 100 mV



Chapter 4. CONCLUSIONS AND FUTURE WORK

4.1 Conclusions

The Synthesis mechanism used for fabrication of Graphene nanostructures has proven its utility for resistive switching devices and can be extended to other 2D materials such as MX₂. For synthesis of nanoflakes and even for quantum dots. The EHDA, roll to plate and screen printing technologies have proved their ability to print resistive switching devices and arrays that can be extended to roll to roll based mass printing. Although only 2x2 array of memristors has been reported here. The modified mesh and plate design can be used to extend the array to higher nxn matrix for multibit memory device. The GQD synthesis technique is compatible with solution based thin film printing systems, and good yield of 7 mg/ml. The GQDs showed strong absorption in UV and deep UV ~290 nm, and luminescence for 365 nm UV lamp, PL emission peaks were observed at 442 and 492 nm for laser excitation wavelengths varied from 300-350 nm. The peak intensities increased with increase in excitation wavelength. The GQD/PVP nanocomposite was successfully synthesized and EHD atomized on target substrate. All printed GQD/PVP nanocomposite memristors in the 3x3 array showed OFF/ON ratio ~10, retentivity of over 24 h, flexibility up to 1.8 cm for over 200 cycles and robustness to withstand more than 100 voltage stress cycles. Graphene nanoflake (20-200 nm) were synthesized from layer graphene flakes, diameter $>1 \,\mu$ m by ultra-sonication, magnetic stirring and mechanical shaker, in presence of NMP. Nanoflake PVP nanocomposite ink formulation was adjusted for EHDA compatibility and EHD atomized on ITO coated PET Transmittance of Graphene nanoflake/PVP nanocomposite thin film was ~87%. SEM and surface profilometry showed uniform thin films. Nanocomposite films, sandwiched between Ag and ITO were studied for resistive switching at 10 nA. The devices showed an excellent OFF/ON ratio of 35:1, tested retentivity up to an hour, flexible enough to withstand 50 continuous bending cycles and showed robustness for at least 40 voltage stress cycles. A non-processed graphene composite deposited using three different approaches was also explored for resistive switching. Three approaches, layer by layer, Semi-blend and blended approaches were used to deposit graphene flakes (unprocessed, exfoliated in NMP) and PVP. Among the three approaches for active layers fabrication, the blended approach was better compared to semi blend and layer by layer approaches. The fabricated devices showed better robustness to multiple voltage sweeps at lower compliance current of 100µA compared to 5mA. The Roff/Ron for devices



fabricated by blended approach was 5 and consistent for more than 20 voltage stress cycles. However the presence of graphene flakes in this form did not enhance the memristive behavior of PVP.

4.2 Future work

The luminescent properties of Graphene quantum dots synthesized in the work presented here are worth further exploration for photonic devices such as photodetectors. The technique employed for synthesis of graphene quantum dots can be explored to other 2D materials for futuristic photonic and electronic devices. The effect of replacing PVP polymer in graphene PVP composite can also be explored to devise resistive switches with higher retentivity and better OFF to ON ratios.



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